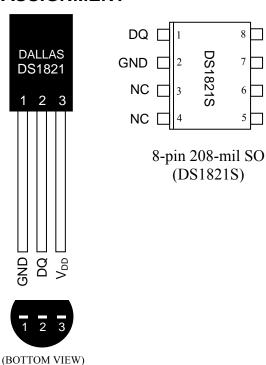


# DS1821 Programmable Digital Thermostat and Thermometer

#### **FEATURES**

- Requires no external components
- Unique 1-Wire<sup>®</sup> interface requires only one port pin for communication
- Operates over a -55°C to +125°C (-67°F to +257°F) temperature range
- Functions as a standalone thermostat with user-definable trip-points
- Provides 8-bit (1°C resolution) centigrade temperature measurements
- Accuracy is  $\pm 1$ °C over 0°C to +85°C range
- Converts temperature to a digital word in 1 second (max)
- Available in 3-pin PR35 and 8-pin SO packages
- Applications include thermostatic controls, industrial systems, consumer products, thermometers, or any thermally sensitive system

#### **PIN ASSIGNMENT**



 $V_{DD}$ 

 $\square$  NC

6 □ NC

5 □ NC

### PIN DESCRIPTION

PR35 (DS1821)

GND - Ground

DQ - Data In/Out and Thermostat Output

V<sub>DD</sub> - Power Supply Voltage

NC - No Connect

#### DESCRIPTION

The DS1821 can function as a standalone thermostat with user-programmable trip-points or as 8-bit temperature sensor with a 1-Wire digital interface. The thermostat trip-points are stored in nonvolatile memory, so DS1821 units can be programmed prior to system insertion for true standalone operation. The DS1821 has an operating temperature range of  $-55^{\circ}$ C to  $+125^{\circ}$ C and is accurate to  $\pm 1^{\circ}$ C over a range of  $0^{\circ}$ C to  $+85^{\circ}$ C. Communication with the DS1821 is accomplished through the open-drain DQ pin; this pin also serves as the thermostat output.

#### ORDER INFORMATION

ORDERING	PACKAGE	DESCRIPTION			
NUMBER	MARKING				
DS1821	DS1821	DS1821 in 3-pin PR35			
DS1821+	DS1821 (See Note)	DS1821 in 3-pin PR35			
DS1821S	DS1821S	DS1821 in 208 mil 8-pin SO			
DS1821S/T&R	DS1821S	DS1821 in 208 mil 8-pin SO, 2000 Piece Tape-and-Reel			
DS1821S+	DS1821S (See Note)	DS1821 in 208 mil 8-pin SO			
DS1821S+T&R	DS1821S (See Note)	DS1821 in 208 mil 8-pin SO, 2000 Piece Tape-and-Reel			

*Note:* A "+" symbol will also be marked on the package.

#### **DETAILED PIN DESCRIPTIONS** Table 1

PR35	8-PIN SO*	SYMBOL	DESCRIPTION
1	2	GND	Ground pin.
2	1	DQ	Open drain data input/output pin – 1-Wire operation; Open drain
			thermostat output pin –thermostat operation.
3	8	$V_{ m DD}$	Power supply pin.

<sup>\*</sup>All pins not specified in this table are "No Connect" pins.

#### **OVERVIEW**

Figure 1 shows a block diagram of the DS1821 and pin descriptions are given in Table 1. The DS1821 can operate as a standalone thermostat with user-programmable trip-points or as 8-bit temperature sensor with a 1-Wire digital interface. The open-drain DQ pin functions as the thermostat output for thermostat operation and as the data I/O pin for 1-Wire communications. The 1-Wire interface provides user access to the nonvolatile (EEPROM) thermostat trip-point registers ( $T_H$  and  $T_L$ ), the status/configuration register, and the temperature register.

When configured as standalone thermostat, temperature conversions start immediately at power-up. In this mode, the DQ pin becomes active when the temperature of the DS1821 exceeds the limit programmed into the  $T_{\rm H}$  register, and remains active until the temperature drops below the limit programmed into the  $T_{\rm L}$  register.

The DS1821 uses Dallas' exclusive 1-Wire bus protocol that implements bus communication with one control signal. This system is explained in detail in the 1-Wire BUS SYSTEM section of this datasheet.

<sup>+</sup> Denotes lead-free package.

#### **ABSOLUTE MAXIMUM RATINGS\***

Voltage on any pin relative to ground -0.5V to +7.0VOperating temperature  $-55^{\circ}C$  to  $+125^{\circ}C$ Storage temperature  $-55^{\circ}C$  to  $+125^{\circ}C$ 

Soldering temperature See-JTD-020A Specification

**DC ELECTRICAL CHARACTERISTICS** (-55°C to +125°C;  $V_{DD}$ =2.7V to 5.5V)

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS	NOTES
Supply Voltage	$V_{DD}$		+2.7		+5.5		1
Thermometer Error	$t_{\rm ERR}$	$0^{\circ}\text{C to } +85^{\circ}\text{C}$ $V_{DD} = 3.6\text{V to } 5.5\text{V}$			±1	°C	2,3,4
		-55°C to $+125$ °C V <sub>DD</sub> = 3.6V to 5.5V		See Typical Curve (Figure 11)			
DQ Logic Low	$V_{ m IL}$		-0.3		+0.8	V	1,5
DQ Logic High	$V_{\mathrm{IH}}$		+2		The lower of	V	1,6
					+5.5		
					or		
					$V_{\rm DD} + 0.3$		
Sink Current	$ m I_L$	$V_{DQ} = 0.4V$	4			mA	1
		$V_{DD} = 3.6 \text{V to } 5.5 \text{V}$					
Standby Current	$I_Q$	-55°C to +85°C		1	3	μA	7
Active Current	$I_{DD}$	$V_{DD} = 5V$		500	1000	μΑ	8
DQ Input	$I_{DQ}$			5		μΑ	9
Current							

#### NOTES:

- 1. All voltages are referenced to ground.
- 2. Thermometer error reflects the sensor accuracy as tested during calibration.
- 3. See typical performance curve in Figure 11 for specification limits outside the 0°C to +85°C range.
- 4. For T<0°C, accuracy degrades by 0.5°C/V for V<sub>DD</sub><4.3V.
- 5. Logic low voltages are specified at a sink current of 4 mA.
- 6. Logic high voltages are specified at a source current of 1 mA.
- 7. Standby current is typically 5 μA at 125°C.
- 8. Active current refers to supply current during active temperature conversions or EEPROM writes.
- 9. DQ line is high ("hi-Z" state).

<sup>\*</sup>These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

AC ELECTRICAL CHARACTERISTICS: (-55°C to +125°C; V<sub>DD</sub>=3.6V to 5.5V)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
Temperature Conversion Time	$t_{CONV}$		0.4	1.0	S	
EEPROM Write Time	$t_{\mathrm{WR}}$		10	50	ms	
Time Slot	$t_{ m SLOT}$	60		120	μs	1
Recovery Time	$t_{ m REC}$	1			μs	1
Write 0 Low Time	$t_{ m LOW0}$	60		120	μs	1
Write 1 Low Time	$t_{\rm LOW1}$	1		15	μs	1
Read Data Valid	$t_{ m RDV}$			15	μs	1
Reset Time High	$t_{ m RSTH}$	480			μs	1
Reset Time Low	$t_{ m RSTL}$	480			μs	1,2
Presence Detect High	t <sub>PDHIGH</sub>	15		60	μs	1
Presence Detect Low	$t_{ m PDLOW}$	60		240	μs	1
V <sub>DD</sub> Low to Mode Toggle Clock Low	$t_{PC}$	100			ns	1,3
Mode Toggle Clock 16 High to V <sub>DD</sub>	$t_{CP}$	100			ns	1
High						
Mode Toggle Clock Pulse Low Time	$t_{ m CL}$	0.1		10	μs	1
Mode Toggle Clock Pulse High Time	$t_{\mathrm{CH}}$	0.1			μs	1
Mode Toggle Clock High-to-Low or	$t_{\mathrm{T}}$			100	ns	1
Low-to-High Transition Time						
Capacitance	C <sub>IN/OUT</sub>			25	pF	

## **NOTES:**

- 1. Refer to timing diagrams in Figure 13.
- 2. If  $t_{RSTL} > 960 \mu s$ , a power-on-reset may occur.
- 3. Time required for part to disable thermostat output.